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To:	Commissioner for Patents	Fax:	(703) 872-9306
From:	Roger C. Knapp	Date:	November 10, 2003
Reg. No:	46,836	Pages:	10
Applicant:	Liam, et al.	Docket No:	00 P 9119 US
Serial No.:	09/751,551	Art Unit:	2815
Filed:	December 28, 2000	Examiner:	Ortiz, E.
For:	Multi-Layer Pt Electrode for DRAM and FRAM with High K Dielectric Materials		

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- Response Pursuant to 37 C.F.R. § 1.116 (9 pages)

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Reply Under 37 C.F.R. 1.116  
Expedited Procedure  
Technology Center 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Lian, *et al.*

Docket No.: 00 P 9119 US

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Response Pursuant to 37 C.F.R. § 1.116

Dear Sir:

The following remarks are offered in response to the Examiner's Final Office Action dated September 30, 2003. They are respectfully submitted as a full and complete response to that Action.